

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	1	(10/357585).APP.	USPAT; USOCR	OR	ON	2008/09/10 19:28
L23	1	20 AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:24
L22	3	20 AND (((design model circuit) with (abstract abstracted abstraction abstracting reduce reduced reducing reduction decrease decreased decreasing shrink shrinking shrunk condense condensed condensing compress compressed compressing compression contract contracted contracting simplify simplified simplifying simplification drop dropped dropping transform transformed transforming transformation)) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:24
L21	1	20 AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:22

L20	9	("20040153308"   "20050086648"   "20050114809"   "20050210433"   "20060190864"   "5953519"   "6643827"   "6728939"   "6957404"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2008/09/10 19:22
L19	0	(10/431780).APP.	USPAT; USOCR	OR	ON	2008/09/10 19:20
L18	1	(5 7 9 12 13 14 15 16)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/09/10 19:18
L17	9	(5 7 9 12 13 14 15 16)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:18
L16	4	(1 2 3 4) AND (diver (rulebase WITH ibm))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:18
L15	3	(1 2 3 4) AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:17
L14	2	(1 2 3 4) AND (Lazy ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:17

L13	3	(1 2 3 4) AND ((external outside outward outer) ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:17
L12	2	(1 2 3 4) AND ((interface interfaced interfacing connection connecting connect connected link linked linking linkage boundary boundaries interaction interactions interacted terminal ) ADJ2 (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:17
L11	11	(1 2 3 4) AND (design model circuit) AND (partition cluster dividing divide division subdividing subdivide subdivision "sub dividing" "sub divide" "sub division" separation separate separating) AND (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:17
L10	0	(1 2 3 4) AND ((design model circuit) SAME (partition cluster dividing divide division subdividing subdivide subdivision "sub dividing" "sub divide" "sub division" separation separate separating) SAME (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16

L9	6	(1 2 3 4) AND (((design model circuit) with (abstract abstracted abstraction abstracting reduce reduced reducing reduction decrease decreased decreasing shrink shrinking shrunk condense condensed condensing compress compressed compressing compression contract contracted contracting simplify simplified simplifying simplification drop dropped dropping transform transformed transforming transformation)) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16
L8	14	(1 2 3 4) AND (((design model circuit) with (abstract abstracted abstraction abstracting reduce reduced reducing reduction decrease decreased decreasing shrink shrinking shrunk condense condensed condensing compress compressed compressing compression contract contracted contracting simplify simplified simplifying simplification drop dropped dropping transform transformed transforming transformation)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16
L7	3	(1 2 3 4) AND (Lazy WITH (constraint assertion checker declaration affirmation "hooked up" forced defined restriction "boolean expression" "logical expression" condition))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16

L6	11	(1 2 3 4) AND (satisfiability SAT) SAME (BMC "bounded model checking" bounded\$1model \$1checking) SAME proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16
L5	8	(1 2 3 4) AND (satisfiability SAT) WITH (BMC "bounded model checking" bounded \$1model\$1checking) WITH proof)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:16
L4	2	(verifi\$6 verify\$3 validat \$3 test\$3 "logic checking" logic \$1checking (co adj verifi \$6) co\$1verifi\$6 (co adj verifi\$3) co\$1verify\$3 (co adj validat\$3) co \$1validat\$3) WITH (satisfiability SAT) WITH (BMC "bounded model checking" bounded \$1model\$1checking) WITH (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:12
L3	14	(verifi\$6 verify\$3 validat \$3 test\$3 "logic checking" logic \$1checking (co adj verifi \$6) co\$1verifi\$6 (co adj verifi\$3) co\$1verify\$3 (co adj validat\$3) co \$1validat\$3) SAME (satisfiability SAT) SAME (BMC "bounded model checking" bounded \$1model\$1checking) SAME (loop looped looping iteration iterate iterating iterated iterative feedback feed\$1back)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:12

L2	0	((design model abstraction abstract abstracting abstracted circuit system Dynamic) with (verifi\$6 verifi\$3 validat\$3 test\$3 "logic checking" logic \$1checking (co adj verifi \$6) co\$1verifi\$6 (co adj verifi\$3) co\$1verifi\$3 (co adj validat\$3) co \$1validat\$3)) SAME (unroll unrolled unrolling un\$1roll un\$1rolled un \$1rolling) SAME (satisfiability SAT) AND Lazy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:12
L1	0	((design model abstraction abstract abstracting abstracted circuit system Dynamic) with (verifi\$6 verifi\$3 validat\$3 test\$3 "logic checking" logic \$1checking (co adj verifi \$6) co\$1verifi\$6 (co adj verifi\$3) co\$1verifi\$3 (co adj validat\$3) co \$1validat\$3)) SAME (unroll unrolled unrolling un\$1roll un\$1rolled un \$1rolling) SAME (satisfiability SAT) SAME Lazy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/10 19:12
S59	26	rulebase WITH ibm	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/26 17:38

9/10/2008 8:25:47 PM

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